

NATIONAL BOARD OF ACCREDITATION

Data Capturing Points of the Program Applied for NBA Accreditation– Tier I/II UG (Engineering) Institute Programs

Program Name : Electronics & Communication Engineering	Discipline : Engineering & Technology
Level : Under Graduate	Tier : 1
Application No : 11321	Date of Submission : 23-12-2025

PART A- Profile of the Institute

A1.Name of the Institute : SAGI RAMAKRISHNAM RAJU ENGINEERING COLLEGE,BHIMAVARAM,ANDHRA PRADESH	
Year of Establishment : 1980,1992	Location of the Institute: SRKR Marg China Amiram
A2. Institute Address :BHIMAVARAM,ANDHRA PRADESH	
City:--Select--	State:Andhra Pradesh
Pin Code:534204	Website:www.srkrec.ac.in
Email:PRINCIPAL@SRKREC.AC.IN	Phone No(with STD Code):08816-223332
A3. Name and Address of the Affiliating University (if any):	
Name of the University : Jawaharlal Nehru Technological University, Kakinad	City: east Godavari
State : Andhra Pradesh	Pin Code: 533003
A4. Type of the Institution : Autonomous CAY(2016-17)	
A5. Ownership Status : Self financing	

A6. Details of all Programs being Offered by the Institution:

- No. of UG programs: **12**
- No. of PG programs: **6**

Table No. A6.1: List of all programs offered by the Institute.

Sr.No.	Discipline	Level of program	Name of the program	Year of Start	Year of Closed	Name of The Department
1	Engineering & Technology	UG	Artificial Intelligence and Data Science	2020	--	Information Technology
2	Engineering & Technology	UG	Artificial Intelligence and Machine Learning	2021	--	Computer Science and Engineering
3	Engineering & Technology	PG	CAD/CAM	2003	--	Mechanical Engineering
4	Engineering & Technology	UG	Civil Engineering	1980	--	Civil Engineering
5	Engineering & Technology	PG	Communication Systems	2006	--	Electronics and Communication Engineering
6	Engineering & Technology	UG	Computer Science & Information Technology	2023	--	Information Technology
7	Engineering & Technology	PG	Computer Science & Technology	2006	--	Computer Science and Engineering
8	Engineering & Technology	UG	Computer Science and Business System	2020	--	Information Technology

9	Engineering & Technology	UG	Computer Science and Design	2021	--	Computer Science and Engineering
10	Engineering & Technology	UG	Computer Science and Engineering	1991	--	Computer Science and Engineering
11	Engineering & Technology	UG	Computer Science and Engineering (Internet of Things and Cyber Security including Blockchain Technology)	2022	--	Computer Science and Engineering
12	Engineering & Technology	UG	Electrical & Electronics Engineering	1994	--	Electrical and Electronics Engineering
13	Engineering & Technology	UG	Electronics & Communication Engineering	1980	--	Electronics and Communication Engineering
14	Engineering & Technology	UG	Information Technology	1999	--	Information Technology
15	Engineering & Technology	PG	Information Technology	2006	--	Information Technology
16	Engineering & Technology	UG	Mechanical Engineering	1980	--	Mechanical Engineering
17	Engineering & Technology	PG	Power Systems & Automation Engineering	2010	--	Electrical and Electronics Engineering
18	Engineering & Technology	PG	Structural Engineering	2009	--	Civil Engineering

A7. Programs to be considered for Accreditation vide this Application:

Table No. A7.1: List of programs to be considered for accreditation.

Name of the Department	Having Allied Departments	Name of the Program	Program Level
Electrical and Electronics Engineering	No	Electrical & Electronics Engineering	UG
Mechanical Engineering	No	Mechanical Engineering	UG
Civil Engineering	No	Civil Engineering	UG
Electronics and Communication Engineering	No	Electronics & Communication Engineering	UG

Table No. A7.2: Allied Department(s) to the Department of the program considered for accreditation as above.
Cluster ID. Name of the Department (in table no. A7.1) Name of allied Departments/Cluster (for table no. A7.1)

No Record

PART-B: Program information

B1. Provide the Required Information for the Program Applied For:

Table No. B1: Program details.

A. List of the Programs Offered by the Department:

SR.NO.	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY APPROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIMES PROGRAM ACCREDITED
1	Electronics & Communication Engineering	UG	1980 / --	240	Yes	2024	240	2024	South-Central/1-43655977681/2024/EOA	Granted accreditation for 3 years for the period (specify period)	2023	2026	5

Sanctioned Intake for Last Five Years for the Communication Systems

Academic Year	Sanctioned Intake
2025-26	240
2024-25	180
2023-24	240
2022-23	240
2021-22	240
2020-21	240

List of the Allied Departments/Cluster and Programs:

B2. Detail of Head of the Department for the program under consideration:

A. Name of the HoD :	Dr. Satti Sudha Mohan Reddy
B. Nature of appointment:	Regular
C. Qualification:	M.Tech and Ph.D.

B3. Program Details

Table No.B3.1: Admission details for the program excluding those admitted through multiple entry and exit points.

Item (Information to be provided cumulatively for all the shifts with explicit headings, wherever applicable)	2025-26 (CAY)	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)	2021-22 (CAYm4)	2020-21 (CAYm5)	2019-20 (CAYm6)
N=Sanctioned intake of the program (as per AICTE /Competent authority)	240	180	240	240	240	240	240
N1=Total no. of students admitted in the 1st year minus the no. of students, who migrated to other programs/ institutions plus no. of students, who migrated to this program	233	174	192	226	239	233	233
N2=Number of students admitted in 2nd year in the same batch via lateral entry including leftover seats	0	23	70	25	24	24	24
N3=Separate division if any	24	18	24	24	24	24	24
N4=Total no. of students admitted in the 1st year via all supernumerary quotas	0	0	0	0	0	0	0

Total number of students admitted in the program (N1 + N2 + N3 + N4) - excluding those admitted through multiple entry and exit points.	257	215	286	275	287	281	281
---	-----	-----	-----	-----	-----	-----	-----

CAY= Current Academic Year. CAYm1= Current Academic Year Minus 1 CAYm2= Current Academic Year Minus 2. LYG= Last Year Graduate. LYGm1= Last Year Graduate Minus 1. LYGm2= Last Year Graduate Minus 2.

B4. Enrolment Ratio in the First Year

Table No. B4.1: Student enrolment ratio in the 1st year.

Year of entry	N (From Table 4.1)	N1 (From Table 4.1)	N4 (From Table 4.1)	Enrollment Ratio [(N1/N)*100]
2025-26 (CAY)	240	233	0	97.08
2024-25 (CAYm1)	180	174	0	96.67
2023-24 (CAYm2)	240	192	0	80.00

Average [(ER1 + ER2 + ER3) / 3] = 91.25≡ 20.00

B5. Success Rate of the Students in the Stipulated Period of the Program

Table No.B5.1: The success rate in the stipulated period of a program.

Item	(2021-22) LYG	(2020-21) LYGm1	(2019-20) LYGm2
A*= (No. of students admitted in the 1st year of that batch and those actually admitted in the 2nd year via lateral entry, plus the number of students admitted through multiple entry (if any) and separate division if applicable, minus the number of students who exited through multiple entry (if any).	287.00	281.00	281.00
B=No. of students who graduated from the program in the stipulated course duration	170.00	156.00	159.00
Success Rate (SR)= (B/A) * 100	59.23	55.52	56.58

Average SR of three batches ((SR_1+ SR_2+ SR_3)/3): 57.11

B6. Academic Performance of the First-Year Students of the Program

Table No.B6.1: Academic Performance of the First-Year Students of the Program.

Academic Performance	CAYm1(2024-25)	CAYm2(2023-24)	CAYm3 (2022-23)
Mean of CGPA or mean percentage of all successful students(X)	7.60	8.30	8.18
Y=Total no. of successful students	164.00	171.00	198.00
Z=Total no. of students appeared in the examination	192.00	216.00	250.00
API [X*(Y/Z)]	6.49	6.57	6.48

Average API[(AP1+AP2+AP3)/3] : 6.51

B7: Academic Performance of the Second Year Students of the Program

Table No.B7.1: Academic Performance of the Second Year Students of the Program.

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 2nd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 2nd year/10)	8.10	8.30	8.28
Y=Total no. of successful students	195.00	161.00	184.00
Z=Total no. of students appeared in the examination	260.00	249.00	264.00
API [X * (Y/Z)]	6.08	5.37	5.77

Average API [(AP1 + AP2 + AP3)/3] : 5.74

B8. Academic Performance of the Third Year Students of the Program

Table No.B8.1: Academic Performance of the Third Year Students of the Program

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 3rd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 3rd year/10)	8.07	8.19	8.18
Y=Total no. of successful students	153.00	171.00	157.00
Z=Total no. of students appeared in the examination	161.00	184.00	165.00
API [$X*(Y/Z)$]:	7.67	7.61	7.78

Average API [(AP1 + AP2 + AP3)/3] : 7.69

B9. Placement, Higher Studies, and Entrepreneurship

Table No.B9.1: Placement, higher studies, and entrepreneurship details.

Item	LYG (2021-22)	LYGm1(2020-21)	LYGm2(2019-20)
FS*=Total no. of final year students	264.00	264.00	264.00
X=No. of students placed	119.00	113.00	214.00
Y=No. of students admitted to higher studies	7.00	5.00	8.00
Z= No. of students taking up entrepreneurship	0.00	0.00	0.00
Placement Index(P) = $((X + Y + Z)/FS) * 100$:	47.73	44.70	84.09

Average Placement Index = (P_1 + P_2 + P_3)/3: 58.84 Placement Index Points:

PART C: Faculty Details in Department and Allied Departments

(Data to be filled in for the Department and Allied Departments)

C1. Faculty details of Department and Allied Departments

Table No.C1: Faculty details in the Department for the past 3 years including CAY

Sr.No	Name of the Faculty	PAN No.	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Nature of Association (Regular/ Contract/ Ad hoc)	Currently Associated (Y/N)	In case of NO, Date of Leaving	IS HOD?
1	Dr. Satti Sudha Mohan Reddy	XXXXXXXX84K	M.Tech and Ph.D.	Andhra University	Antennas	01/11/2000	25.1	Assistant Professor	Professor	03/01/2022	Regular	Yes		Yes
2	Dr. Nadakuduru Udaya Kumar	XXXXXXXX11D	M.Tech and Ph.D.	JNTUH	Image processing	01/10/1992	33.2	Lecturer	Professor	01/12/2006	Regular	Yes		No
3	Dr. Bhupathiraju Venkata Satya Suryanarayana Raju	XXXXXXXX79H	M.E. and Ph.D.	Osmania University	Electronics and Communication Engineering	22/06/1992	33.5	Lecturer	Professor	01/12/2006	Regular	Yes		No

4	Gannabathula Venkata Someswara Padma Rao	XXXXXXXX52P	M.Tech	Guru Jambheshwar University	Electronics and Communication Engineering	15/10/1997	28.2	Assistant Professor	Professor	01/12/2006	Regular	Yes		No
5	Dr. Yarramala Ramalakshmanna	XXXXXXXX65A	M.Tech and Ph.D.	Annamalai University	Bio Medical Signal Processing	27/11/2000	25	Assistant Professor	Associate Professor	01/01/2009	Regular	Yes		No
6	Dr. Gubbala Naga Raju	XXXXXXXX87R	M.E. and Ph.D.	KL deemed to be university	Image processing	12/07/2004	21.4	Assistant Professor	Associate Professor	05/08/2023	Regular	Yes		No
7	Dr. Tirumani V.Hyma Lakshmi	XXXXXXXX39F	M.Tech and Ph.D.	KL deemed to be university	Image processing	28/01/2006	19.10	Assistant Professor	Associate Professor	16/10/2023	Regular	Yes		No
8	Dr. Bala Sindhuri Kandula	XXXXXXXX92R	M.Tech and Ph.D.	JNTUK	VLSI and Signal Processing	03/01/2011	14.11	Assistant Professor	Associate Professor	15/02/2024	Regular	Yes		No
9	Dr. Rudraraju Krishna Chaitanya	XXXXXXXX07K	M.Tech and Ph.D.	Andhra University	Antennas	17/06/2005	20.5	Assistant Professor	Associate Professor	09/08/2024	Regular	Yes		No
10	Dr. Bandi Sanjay	XXXXXXXX48L	M.Tech and Ph.D.	KL deemed to be university	Antennas	01/10/2012	13.2	Assistant Professor	Associate Professor	25/08/2025	Regular	Yes		No
11	Talari Venkata Syamala Raju	XXXXXXXX34M	M.Tech	Andhra University	Communication Systems	10/07/2007	18.5	Assistant Professor	Assistant Professor		Regular	Yes		No
12	Dr. Penmatsa Krishna Kanth Varma	XXXXXXXX09N	M.Tech and Ph.D.	Visvesvaraya Technological University	Antennas	01/09/2009	16.3	Assistant Professor	Assistant Professor		Regular	Yes		No
13	Kalidindi N.V. Suresh Varma	XXXXXXXX81G	M.Tech	Nagarjuna University	Communication & Radar	08/11/2011	14.1	Assistant Professor	Assistant Professor		Regular	Yes		No
14	Buddaraju Revathi	XXXXXXXX55K	M.Tech	Andhra University	Communication Systems	02/07/2012	13.5	Assistant Professor	Assistant Professor		Regular	Yes		No
15	Dr. Kanuboyina Satyanarayana Naga V	XXXXXXXX25E	M.Tech and Ph.D.	Annamalai University	Biomedical Image and Signal Processing	02/07/2012	13.5	Assistant Professor	Assistant Professor		Regular	Yes		No
16	Gamini Prathima	XXXXXXXX35H	M.Tech	Andhra University	Communication Systems	13/07/2012	13.4	Assistant Professor	Assistant Professor		Regular	Yes		No
17	Thammisetty V.Narayana	XXXXXXXX89H	M.Tech	Andhra University	Communication Systems	01/10/2012	13.2	Assistant Professor	Assistant Professor		Regular	Yes		No
18	Vadlamudi Ramakrishna	XXXXXXXX27F	M.Tech	Andhra University	Communication Systems	01/10/2012	13.2	Assistant Professor	Assistant Professor		Regular	Yes		No
19	Bugge Bhagya Prasad	XXXXXXXX63M	M.Tech	Andhra University	Communication Systems	01/10/2012	13.2	Assistant Professor	Assistant Professor		Regular	Yes		No
20	Dantuluri Bhavani	XXXXXXXX17P	M.Tech	JNTUH	VLSID	15/07/2015	10.4	Assistant Professor	Assistant Professor		Regular	Yes		No

21	Dr. Kanumuri Chalapathi Raju	XXXXXXX03R	M.Tech and Ph.D.	Visvesvaraya Technological University	Image Processing	15/07/2015	10.4	Assistant Professor	Assistant Professor		Regular	Yes		No
22	D. V N. Bharathi	XXXXXXX08H	M.Tech	Andhra University	Communication Systems	15/07/2015	10.4	Assistant Professor	Assistant Professor		Regular	Yes		No
23	Dr. Jampani Ravi	XXXXXXX82G	M.Tech and Ph.D.	Sathyabama University	Image processing	15/07/2016	9.4	Assistant Professor	Assistant Professor		Regular	Yes		No
24	Kolli Venkatrao	XXXXXXX86P	M.Tech	Andhra University	Communication Systems	18/07/2016	9.4	Assistant Professor	Assistant Professor		Regular	Yes		No
25	Bokka Tapasvi	XXXXXXX69G	M.Tech	Andhra University	Communication Systems	10/07/2015	10.4	Assistant Professor	Assistant Professor		Regular	Yes		No
26	Dr. Vegesna Nagavalli	XXXXXXX97G	M.Tech and Ph.D.	Annamalai University	Antennas	02/11/2016	9.1	Assistant Professor	Assistant Professor		Regular	Yes		No
27	Kalidindi Lakshmi Divya	XXXXXXX36G	M.Tech	JNTUK	VLSID	02/11/2016	9.1	Assistant Professor	Assistant Professor		Regular	Yes		No
28	Penmetsa Kanaka Raju	XXXXXXX91C	M.Tech	JNTUK	VLSI and Embedded Systems	02/11/2016	9.1	Assistant Professor	Assistant Professor		Regular	Yes		No
29	Nakka Kishore Chandra Dev	XXXXXXX16D	M.Tech	Andhra University	Communication Systems	04/11/2016	9.1	Assistant Professor	Assistant Professor		Regular	Yes		No
30	Tirumani Nalini Prasad	XXXXXXX24K	M.Tech	Andhra University	Communication Systems	12/04/2017	8.7	Assistant Professor	Assistant Professor		Regular	Yes		No
31	M.Praveen Kumar	XXXXXXX61D	M.Tech	Andhra University	Communication Systems	04/11/2016	9.1	Assistant Professor	Assistant Professor		Regular	Yes		No
32	Nandula Venkata Phani Sai Kumar	XXXXXXX59M	M.Tech	Andhra University	Communication Systems	17/06/2017	8.5	Assistant Professor	Assistant Professor		Regular	Yes		No
33	Y Sai Sundara Sriramam	XXXXXXX05P	M.Tech	JNTUK	Electronics and Communication Engineering	27/11/2019	6	Assistant Professor	Assistant Professor		Regular	Yes		No
34	K.Phani Varma	XXXXXXX18D	M.Tech	JNTUH	VLSI	02/03/2020	5.9	Assistant Professor	Assistant Professor		Regular	Yes		No
35	R. Devi	XXXXXXX16N	M.Tech	JNTUK	VLSI	15/03/2021	4.8	Assistant Professor	Assistant Professor		Regular	Yes		No
36	Dr. S Swathi	XXXXXXX26Q	M.Tech and Ph.D.	Nirwan University	VLSI	01/04/2021	4.8	Assistant Professor	Assistant Professor		Regular	Yes		No
37	P S S N Mowlika	XXXXXXX24J	M.Tech	JNTUK	VLSI	23/02/2022	3.9	Assistant Professor	Assistant Professor		Regular	Yes		No
38	Dr. A V S Surya Varma	XXXXXXX40A	M.Tech and Ph.D.	KL deemed to be university	VLSI and Communications	01/11/2022	3.1	Assistant Professor	Assistant Professor		Regular	Yes		No

39	Dr.M Satish Kumar	XXXXXXX63L	M.Tech and Ph.D.	Acharya Nagarjuna University	Antennas:EMI/EMC in Design	01/12/2022	3	Assistant Professor	Assistant Professor		Regular	Yes		No
40	D.Rajeswari	XXXXXXX11G	M.Tech	JNTUK	Digital Electronics and Communication Systems	01/08/2023	2.4	Assistant Professor	Assistant Professor		Regular	Yes		No
41	Mudunuri Vijaya Rama Raju	XXXXXXX06A	M.E.	Andhra University	Electronics and Instrumentation	20/11/1998	26.5	Assistant Professor	Associate Professor	01/11/2008	Regular	No	16/04/2025	No
42	Dr. U. Sreenivasulu	XXXXXXX68A	M.Tech and Ph.D.	JNTUH	Wireless Communication and Networks	04/11/2022	1.8	Assistant Professor	Assistant Professor		Regular	No	22/07/2024	No

Table No.C2: Faculty details of Allied Departments for the past 3 years including CAY.

C2. Student-Faculty Ratio (SFR)

No. of UG(Engineering) programs in Department including allied departments/ clusters (UGn):

UG1=1st UG program

UGn=nth UG program

B= No. of Students in UG 2nd year (ST)

C= No. of Students in UG 3rd year (ST)

D= No. of Students in UG 4th year (ST)

No. of PG (Engineering) programs in Department including allied departments/ clusters (PGm):

PG1=1st PG program.

PGm=mth PG program

A= No. of Students in PG 1st year

B= No. of Students in PG 2nd year

Student Faculty Ratio (**SFR**) = S/F

S= No. of students of all programs in the Department including all students of allied departments/clusters.

No. of students (ST)=Sanctioned Intake (SA)+ Actual admitted students via lateral entry including leftover seats (L) if any (limited to 10 % of SA)

Students who admitted under supernumerary quotas (SNQ, EWS, etc) will not be considered in calculating SFR value. Those students are exempted.

F=Total no. of regular or contractual faculty members (Full Time) in the Department, including allied departments/clusters (excluding first year faculty (The faculty members who have a 100% teaching load in the first-year courses)).

No. of UG Programs in the Department1 No. of PG Programs in the Department1

Table No.C2.1: Student-faculty ratio.

Description	CAY(2025-26)	CAYm1 (2024-25)	CAYm2 (2023-24)
UG1.B	198	264	264
UG1.C	264	264	264
UG1.D	264	264	264
UG1: Electronics & Communication Engineering	726	792	792
PG1.A	6	6	6
PG1.B	6	6	6
PG1: Communication Systems	12	12	12
DS=Total no. of students in all UG and PG programs in the Department	738	804	804
AS=Total no. of students of all UG and PG programs in allied departments	0	0	0
S=Total no. of students in the Department (DS) and allied departments (AS)	S1= 738	S2= 804	S3= 804

Description	CAY(2025-26)	CAYm1 (2024-25)	CAYm2 (2023-24)
DF=Total no. of faculty members in the Department	40	40	42
AF= Total no. of faculty members in the allied Departments	0	0	0
F=Total no. of faculty members in the Department (DF) and allied Departments (AF)	F1= 40	F2= 40	F3= 42
FF=The faculty members in F who have a 100% teaching load in the first-year courses	2	2	2
Student Faculty Ratio (SFR)=S/(F-FF)	SFR1= 19.42	SFR2= 21.16	SFR3= 20.10
Average SFR for 3 years	SFR= 20.23		

C3. Faculty Qualification

- Faculty qualification index (FQI) = $2.5 * [(10X + 4Y)/RF]$ where
- X=No. of faculty members with Ph.D. degree or equivalent as per AICTE/UGC norms.
- Y=No. of faculty members with M. Tech. or ME degree or equivalent as per AICTE/ UGC norms.
- RF=No. of required faculty in the Department including allied Departments to adhere to the 20:1 Student-Faculty ratio, with calculations based on both student numbers and faculty requirements as per section C2 of this documents: (RF=S/20).

Table No.C3.1: Faculty qualification.

Year	X	Y	RF	FQ = $2.5 \times [(10X + 4Y) / RF]$
2025-26(CAY)	15	25	36.00	17.36
2024-25(CAYm1)	12	28	40.00	14.50
2023-24(CAYm2)	10	32	40.00	14.25

C4. Faculty Cadre Proportion

- Faculty Cadre Proportion is 1(RF1): 2(RF2): 6(RF3)
- RF1= No. of Professors required = $1/9 * \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per C2 of this documents.}$
- RF2= No. of Associate Professors required = $2/9 * \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents.}$
- RF3= No. of Assistant Professors required = $6/9 * \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents.}$
- Faculty cadre and qualification and experience should be as per AICTE/UGC norms.

Table No.C4.1: Faculty cadre proportion details.

Year	Professors		Associate Professors		Assistant Professors	
	Required RF1	Available AF1	Required RF2	Available AF1	Required RF3	Available AF3
2025-26	4.00	3.00	8.00	5.00	24.00	32.00
2024-25	4.00	3.00	8.00	4.00	26.00	33.00
2023-24	4.00	3.00	8.00	1.00	26.00	38.00
Average	RF1=4.00	AF1=3.00	RF2=8.00	AF2=3.33	RF2=25.33	AF2=34.33

C5. Visiting/Adjunct Faculty/Professor of Practice

Table No. C5.1: List of visiting/adjunct faculty/professor of practice and their teaching and practical loads.

(CAYm1)

(CAYm2)

(CAYm3)

C6. Academic Research

Table No. C6.1: Faculty publication details.

S.No.	Item	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)
1	No. of peer reviewed journal papers published	20	21	24
2	No. of peer reviewed conference papers published	39	25	14
3	No. of books/book chapters published	10	13	6

C7. Sponsored Research Project

Table No. C7.1: List of sponsored research projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.G Harish Kumar Varma	Dr.K.Chalapathi Raju	SRKREC	Science Technology and Innovation Driven Inland Aquaculture Hub for Bhimavaram Suburbs	DST	2024-25	96.01
						Amount received (Rs.):96.01

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.N.Udaya Kumar		SRKREC,ECE	Drones in Disaster Management	MSME	2 Years	8.00
						Amount received (Rs.):8.00

(CAYm3)

Total Amount (Lacs) Received for the Past 3 Years: 104.01

Note*:

- Only sponsored research projects will be considered. Infrastructure-based projects will not be considered here.

C8. Consultancy Work

Table No. C8.1: List of consultancy projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.N.Udaya Kumar	Dr.K.Bala sindhuri, Sri.K.Venkatrao	ECE	Audio Signal Pre-Processing	Salicit Technologies Private Limited, Hyderabad	3 Years	1.74
						Amount received (Rs.):1.74

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.N.Udaya Kumar	Dr.K.Bala sindhuri, Sri.K.Venkatrao	ECE	Audio Signal pre-processing	Salicit Technologies Private Limited, Hyderabad	2 Years	1.74
Dr. B T P Madhav	Dr. S S Mohan Reddy	ECE	Development of Fabric/Textile Substrate Based MIMO antennas and Frequency Selective Surfaces for Wireless Body Area Network (WBAN) Applications	BHASKAR ENTERPRISES, ADAVINEKKALAM, KRISHNA DIST.	1 Month	0.45
						Amount received (Rs.):2.19

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.N.Udaya Kumar	Dr.K.Bala sindhuri, Sri.K.Venkatrao	ECE	Audio Signal pre-processing	Salicit Technologies Private Limited, Hyderabad	2 Years	1.08
						Amount received (Rs.):1.08

Total amount (Lacs) received for the past 3 years: 5.01

Note*:

- Only consultancy projects will be considered. Infrastructure-based projects will not be considered here.

C9. Institution Seed Money or Internal Research Grant to its Faculty for Research Work

Table No. C9.1: List of faculty members received seed money or internal research grant from the Institution.

(CAYm1)

(CAYm2)

(CAYm3)

Total amount (Lacs) received for the past 3 years :

PART D: Laboratory Infrastructure in the Department

(Data to be filled in for the Department)

D1. Adequate and Well-Equipped Laboratories, and Technical Manpower

Table No.D1.1: List of laboratories and technical manpower.

Sr. No	Name of the Laboratory	Number of students per set up(Batch Size)	Name of the Important Equipment	Weekly utilization status(all the courses for which the lab is utilized)	Technical Manpower Support		
					Name of the Technical staff	Designation	Qualification
1	Communications lab (T-108) Ground floor, T-Block	3	Hardware: 1. Microwave bench set up 2.UPS 3. Oscilloscope 4. ATS –B Basic Antenna Trainer 5. Electronic Trainer Kit 6. VOM 7. Multimeter	Semester-I 54.	P. Eswara Rao	Technician	Diploma (B.Tech)
2	Skill development lab (T-109) Ground floor, T-Block	1	Hardware: 1. Computers 2. UPS 3. Routers	Semester-I 36.	Penmetsa Surendra Var	Technician	ITI
3	Electronics lab-1 (T-208) First Floor T-Block	3	Hardware: 1. Computers 2. Function generators 3. Dual trace CROs 4. Bread board Trainer Kit 5. Digital Multimeter 6. Ammeter 7. Voltmeter	Semester-I 72.	Jampana Ganesh Raju	Technician	ITI
4	Electronics lab-2 (T-209) First Floor T-Block	3	Hardware: Computers Function generators Dual trace CROs Bread board Trainer Kits Digital Multimeter Ammeter Voltmeter Oscilloscope	Semester-I 36.	Jampana Ganesh Raju	Technician	BSc. Computers
5	Digital ICs & Microprocessor lab. (T-203) First Floor T-Block	3	Hardware: Computers Phy-450 Digital logic trainer kit 8085 based microprocessor Trainer with m2 8086 based microprocessor Trainer with m2	Semester-I 72.	Kuriseti Venkata Satish	Technician	ITI electrician
6	Digital signal processing lab (T-304) Second Floor T-Block	1	Hardware: Computers UPS USB-32 boards Sensors Aurdino Boards	Semester-I 3	T Durga Prasad	Technician	B.Tech (ECE)
7	VLSI and IOT lab (T-406) Second Floor T-Block	1	Hardware: Computer UPS Kintex KC 705 evaluation board ZedBoard Artix 7 Board Software: Cadence Xilinx ISE Standard board	Semester-I 72.	Penmetsa Dilip Varma	Technician	Diploma
8	Electronics lab-3 (T-405) Second Floor T-Block	3	Hardware: Function generators Dual trace CROs Bread board Trainer Kits Digital multimeters Capacitor	Semester-I-Nil	K. Rambabu	Senior Technician	ITI
9	Communication Engineering lab-2 Ground floor U-102	3	Hardware: Optical Fiber Trainer Kit Data communication Trainer kit. Wireless LAN trainer Kit Computer Dual trace CROs Digital communication	Semester-I 72.	P Srinivasa Rao	Technician	B.SC Electronics
10	Digital Signal Processing Lab U-107 Ground floor	1	Hardware: 1.Computers 2. UPS 3. Routers	Semester-I 72.	P Srinivasa Rao	Technician	B.SC Electronics
11	Innovation center Ground floor T-110	1	Hardware: 1.Computers 2.My smart phone kits	Throughout the	P. Eswara Rao	Technician	Diploma (B.Tech)

D2. Safety Measures in Laboratories

Table No. D2.1: List of various safety measures in laboratories.

Sr. No	Laboratory Name	Safety Measures
1	Microwave Engineering Lab. (T-108)	<ul style="list-style-type: none"> ● Use of cell phones is prohibited. ● Damaged equipment is identified and serviced at the earliest. ● Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. ● Well trained technical supporting staff always monitor the labs. Students are guided: ● To make proper circuit connections to avoid short- circuits and loose connections. ● Never make wiring changes on live circuits. ● Make circuit connections with all power sources turned off. ● Check if the supply voltage applied is the appropriate value or not.

2	Skill development lab (T-109)	<ul style="list-style-type: none"> ● Use of cell phones is prohibited. ● Damaged equipment is identified and serviced at the earliest. ● Appropriate storage areas are available. ● Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. ● Well trained technical supporting staff always monitor the labs. Students are guided to: ● Not to open the computer's power supply or monitor. ● Always shut down the computer properly when not in use.
3	Electronics lab-1 (T-208)	<ul style="list-style-type: none"> ● Use of cell phones is prohibited. ● Damaged equipment is identified and serviced at the earliest. ● Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. ● Well trained technical supporting staff always monitor the labs. Students are guided: ● To make proper circuit connections to avoid short- circuits and loose connections. ● Never make wiring changes on live circuits. ● Make circuit connections with all power sources turned off. ● Check if the supply voltage applied is the appropriate value or not. ● Not to open the computer's power supply or monitor. ● Always shut down the computer properly when not in use.
4	Electronics lab-2 (T-209)	<ul style="list-style-type: none"> ● Use of cell phones is prohibited. ● Damaged equipment is identified and serviced at the earliest. ● Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. ● Well trained technical supporting staff always monitor the labs. Students are guided: ● To make proper circuit connections to avoid short- circuits and loose connections. ● Never make wiring changes on live circuits. ● Make circuit connections with all power sources turned off. ● Check if the supply voltage applied is the appropriate value or not. ● Not to open the computer's power supply or monitor. ● Always shut down the computer properly when not in use
5	Digital ICs & Microprocessor lab. (T-203)	<ul style="list-style-type: none"> ● Use of cell phones is prohibited. ● Damaged equipment is identified and serviced at the earliest. ● Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. ● Well trained technical supporting staff always monitor the labs. Students are guided: ● To make proper circuit connections to avoid short- circuits and loose connections. ● Never make wiring changes on live circuits. ● Make circuit connections with all power sources turned off. ● Check if the supply voltage applied is the appropriate value or not. ● Not to open the computer's power supply or monitor. ● Always shut down the computer properly when not in use
6	Digital signal processing lab (T-304)	<ul style="list-style-type: none"> ● Use of cell phones is prohibited. ● Damaged equipment is identified and serviced at the earliest. ● Appropriate storage areas are available. ● Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. ● Well trained technical supporting staff monitor the labs at all times. Students are guided to: ● Not to open the computer's power supply or monitor. ● Always shut down the computer properly when not in use
7	Electronics lab-3 (T-405)	<ul style="list-style-type: none"> ● Use of cell phones is prohibited. ● Damaged equipment is identified and serviced at the earliest. ● Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. ● Well trained technical supporting staff always monitor the labs. Students are guided: ● To make proper circuit connections to avoid short- circuits and loose connections. ● Never make wiring changes on live circuits. ● Make circuit connections with all power sources turned off. ● Check if the supply voltage applied is the appropriate value or not. ● Not to open the computer's power supply or monitor. ● Always shut down the computer properly when not in use
8	VLSI AND IOT Lab	<ul style="list-style-type: none"> ● Use of cell phones is prohibited. ● Damaged equipment is identified and serviced at the earliest. ● Appropriate storage areas are available. ● Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. ● Well trained technical supporting staff always monitor the labs. Students are guided to: ● Not to open the computer's power supply or monitor. ● Always shut down the computer properly when not in use
9	Communication Engineering lab-2 (U-102)	<ul style="list-style-type: none"> ● Use of cell phones is prohibited. ● Damaged equipment is identified and serviced at the earliest. ● Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. ● Well trained technical supporting staff always monitor the labs. Students are guided: ● To make proper circuit connections to avoid short- circuits and loose connections. ● Never make wiring changes on live circuits. ● Make circuit connections with all power sources turned off. ● Check if the supply voltage applied is the appropriate value or not. ● Not to open the computer's power supply or monitor. ● Always shut down the computer properly when not in use ● Damaged equipment is identified and serviced at the earliest.
10	Digital Signal Processing lab	<ul style="list-style-type: none"> ● Use of cell phones is prohibited. ● Damaged equipment is identified and serviced at the earliest. ● Appropriate storage areas are available. ● Specific Safety Rules like Do's and Don'ts are displayed and instructed for all students. ● Well trained technical supporting staff always monitor the labs. Students are guided to: ● Not to open the computer's power supply or monitor. ● Always shut down the computer properly when not in use
11	Innovation center	<ul style="list-style-type: none"> ● Lab is facilitated with good ventilation. Students are guided to: ● Not to open the computer's power supply or monitor. ● Always shut down the computer properly when not in use ● Never make wiring changes on live circuits. ● Make circuit connections with all power sources turned off. ● Check if the supply voltage applied is the appropriate value or not.

Project Laboratory	
Experiential Learning Activity Lab T-404	<ol style="list-style-type: none"> Power Supplies, UPS, and Stabilizers: Provide uninterrupted and regulated power for various project setups and electronic circuits. They ensure safe and stable operation of sensitive devices during testing and experimentation. CROs (Cathode Ray Oscilloscopes) and Signal Generators: Facilitate real-time observation of waveforms, signal analysis, and testing of analog/digital circuits. Students use them for debugging, verification, and validation of circuit behaviour. LCR Meter, Multimeters, and Auto Transformers: Help in accurate measurement of component values and circuit parameters essential for prototype testing, component characterization, and calibration. Spectrum Analyzer and Pulse Generators: Enable RF and communication-based projects by allowing frequency spectrum analysis, signal characterization, and verification of transmission/reception quality. Breadboard Trainers and Project Boards (Scienteck): Allow students to prototype circuits quickly, perform circuit debugging, and test modules before final PCB implementation. These are extensively used in microcontroller, analog, and digital projects. Computing Systems (CPU Vostro, Monitors, Keyboards, Mouse): Used for programming, simulation, and interfacing of microcontrollers, microprocessors, and embedded systems. They also support circuit design software and documentation work. Antenna RF Tx/Rx Systems and Stepper Motor Controller: Facilitate experimentation in wireless communication and control system projects. Students develop and test applications involving IoT, robotics, and motor control. Ahuja Wireless Audio System and TV/Display Units: Support multimedia, communication, and demonstration-based projects. They are useful for presentation, signal transmission testing, and evaluation. Solar Plate and Motor: Aid in renewable energy and power electronics-based projects. Students can practically test solar energy conversion, motor drive systems, and efficiency analysis. Benchtop DMM and FM Radio Receiver Kits: Support precise measurement and testing of analog and RF circuits. Students gain hands-on exposure to measurement instruments and signal processing. Furniture and Lab Infrastructure (Experiment Tables, Component Racks, A/C Units): Provide a comfortable and organized workspace for students to perform experiments efficiently and safely, enhancing concentration and productivity. <p>These facilities collectively enable students to design, simulate, implement, and test project ideas in domains such as embedded systems, IoT, robotics, communication systems, and renewable energy</p>
Research Laboratory	
Innovation and product Development Facility	<p>The Research Laboratory is equipped with essential hardware and supporting infrastructure to facilitate programming, simulation, and project development activities. The major equipment and their contributions are as follows:</p> <ol style="list-style-type: none"> CPU and Monitors (Intex / Dell): High-performance computers are used for software development, database programming, and simulation tasks. They support various tools such as Python, MATLAB, enabling students to design, test, and debug software and hardware-interfaced projects. UPS (3KVA – Vertiv) with Battery Backup and Stand: Provides continuous and stable power supply to all systems, ensuring uninterrupted learning and project work, especially during lengthy simulations and testing operations.

Centre of Excellence	
Centre of excellence in VLSI	<p>The Centre of Excellence is well-equipped with advanced hardware, computing systems, and supporting infrastructure to facilitate students' research and development activities. These facilities enable students to design, simulate, and implement complex systems across domains such as VLSI, embedded systems, computer networks, and digital signal processing. The major equipment and their usefulness are as follows:</p> <ol style="list-style-type: none"> 1. Cadence Software Suite is a comprehensive EDA (Electronic Design Automation) tool used for VLSI design and verification, enabling students to perform end-to-end IC design from schematic capture to layout and simulation. It is a major industry-standard tool widely adopted in semiconductor and electronic design companies. 2. KINTEX KC705 FPGA Development Boards These high-performance FPGA boards are utilized for hardware design, digital system prototyping, and embedded system development. They enable students to implement and test complex algorithms in hardware, such as image processing, digital communication, and reconfigurable computing projects. The boards support HDL programming and interface with various peripherals, fostering learning in VLSI design, hardware acceleration, and SoC-based development. 3. DELL OPTIPLEX 5060 Workstation PCs (with Monitors, Keyboards, and Mouse) The lab is equipped with high-speed computers used for software simulation, HDL coding, hardware interfacing, and data analysis. These systems support CAD tools such as Xilinx Vivado, MATLAB/Simulink, Keil, and ModelSim, which are essential for the design and analysis of embedded and hardware-based projects. Students use them for both simulation and integration with FPGA boards. 4. CISCO SG-95-24 Switch & D-LINK Rack Mount Units These networking devices facilitate projects related to computer networking, IoT communication, and network management. They enable students to set up and test LAN, VLAN, and network topology configurations for research in network protocols and server-based communication. 5. APC 6KVA Online UPS with Battery Rack Provides reliable power backup to all the systems, ensuring uninterrupted operation during hardware configuration and long simulation runs. This enhances system reliability and data integrity during research experiments. 6. EPSON Projector & Wireless LAN Module Used for effective presentation and demonstration of research outcomes. The projector supports group discussions, seminars, and technical reviews, helping students to collaborate and communicate their work effectively. 7. Networking Setup Supports remote monitoring and assists in demonstrating IoT-based surveillance projects. Students can use the system for practical implementation of real-time video streaming and security applications. 8. CANON G1010 Colour Printer: Facilitates documentation, research report preparation, and printing of circuit schematics and research papers. This enables efficient presentation of project results and outcomes. 9. Air Conditioners: Maintain optimal temperature for both electronic equipment and comfortable working conditions for students during prolonged experimental sessions. <p>The Research Laboratory significantly enhances students' capability to engage in hardware-software co-design, FPGA-based research, and network simulation projects. It supports the development of analytical, design, and experimental skills, preparing students for industry-oriented and research-based careers. The availability of high-performance computing and advanced hardware platforms enables students to transform innovative ideas into tangible prototypes.</p>

Centre of Excellence: Embedded Systems		
Components provided by Vector India		
Sno	Components	Quantity

1	LPC 2148 DEVELOPMENT BOARD	5
2	STM 32 F103RBT6	5
3	16*2 LCD	5
4	OLED 0.96"	5
5	TEMP-LM35	5
6	SOIL MOISTURE	5
7	BMP 180	5
8	MAX 30100	5
9	IR	5
10	ULTRASONIC	5
11	ADXL 345	5
12	ESP8266	5
13	DC MOTORS WITH DRIVER	5
14	STEPPER MOTORS WITH DRIVER	5
15	BLUETOOTH HC-05	5
16	CONNECTORS FOR INTERFACING	50
17	USB CABLES	10
18	System:i5 12 th Gen,8GB Ram,256 SSD, Windows 11	5
19	19STM32 CUBE IDE Installed	5

PART E: First Year faculty and financial Resources

(Data to be filled in for the first year course faculty and budget allocation and utilization)

E1. First Year Student-Faculty Ratio (FYSFR)

Table No. E1.1: FYSFR details.

Year	Sanctioned intake of all UG programs (S4)	No. of required faculty (RF4= S4/20)	No. of faculty members in Basic Science Courses & Humanities and Social Sciences including Management courses (NS1)	No. of faculty members in Engineering Science Courses (NS2)	Percentage= No. of faculty members $\frac{((NS1 \times 0.8) + (NS2 \times 0.2))}{(\text{No. of required faculty (RF4)})}$; Percentage= $\frac{((NS1 \times 0.8) + (NS2 \times 0.2))}{RF}$
2023-24(CAYm2)	1860	93	87	20	79

2024-25(CAYm1)	1860	93	87	20	79
2025-26(CAY)	1860	93	84	20	77

E2. Budget Allocation, Utilization, and Public Accounting at Institute Level

Table No. E2.1: Budget and actual expenditure incurred at Institute level.

Items	Budgeted in 2024-2025	Actual Expenses in 2024-2025 till	Budgeted in 2023-2024	Actual Expenses in 2023-2024 till	Budgeted in 2022-2023	Actual Expenses in 2022-2023 till	Budgeted in 2021-2022	Actual Expenses in 2021-2022 till
Infrastructure Built-Up	140000000	77598230	150000000	146202039	90000000	79014295	75000000	67938104
Library	4000000	154305	4000000	3211149	4000000	3084585	4000000	2497758
Laboratory equipment	20000000	5902057	19200000	14479485	50000000	32309745	30000000	28359698
Teaching and non-teaching staff salary	520000000	265346910	510000000	502955822	500000000	472269762	465000000	464400355
Outreach Programs	2000000	1490762	2000000	1553420	500000	280080	100000	61507
R&D	2500000	1512200	2500000	2205217	1500000	1398318	1200000	1060302
Training, Placement and Industry linkage	33000000	14139740	32000000	30398673	37600000	34800667	39000000	37709910
SDGs	2000000	512085	3000000	2659146	2000000	1287536	1200000	1033608
Entrepreneurship	400000	195000	400000	335000	400000	392196	400000	287417
Repairs & Maintenance, University Fees ,Reg, taxes	200000000	77775096	200000000	174226563	158500000	137931882	149000000	143356145
Total	923900000	444626385	923100000	878226514	844500000	762769066	764900000	746704804

E3. Budget Allocation, Utilization, and Public Accounting at Program Specific Level

Table No. E3.1: Budget and actual expenditure incurred at program level.

Items	Budgeted in 2024-2025	Actual Expenses in 2024-2025 till	Budgeted in 2023-2024	Actual Expenses in 2023-2024 till	Budgeted in 2022-2023	Actual Expenses in 2022-2023 till	Budgeted in 2021-2022	Actual Expenses in 2021-2022 till
Laboratory equipment	600000	194753	600000	464349	1000000	974655	500000	482544
Software	0	0	0	0	1100000	1097400	0	0
SDGs	100000	0	125000	119566	250000	225023	50000	40000
Support for faculty development	100000	41850	25000	19566	0	0	0	0

R & D	50000	11350	50000	50000	50000	0	100000	100000
Industrial Training, Industry expert, Internship	150000	40000	150000	42016	150000	138086	150000	141000
Maintenance & Spares and Skill Development Lab	300000	157389	300000	155483	3800000	3760045	800000	781105
Total	1300000	445342	1250000	850980	6350000	6195209	1600000	1544649